

2.5V/3.3V, High Bandwidth, Hot-Insertion, 10-Bit, 2-Port, Bus Switch

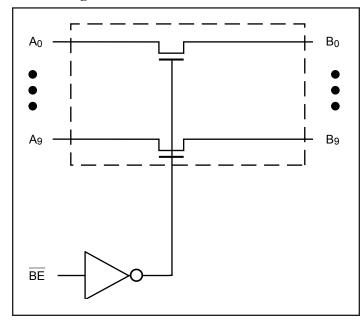
Features

- Near-Zero propagation delay
- 5-ohm switches connect inputs to outputs
- High bandwidth (>400 MHz)
- Rail-to-rail 2.5V or 3.3V switching
- 5V I/O Tolerant
- 2.5V Supply Voltage Operation
- Permits Hot-Insertion
- Packaging (Pb-free & Green Available):
 - 24-pin 150-mil wide plastic QSOP (Q)
 - 24-pin 173-mil wide plastic TSSOP (L)

Applications

- · High bandwidth data switching
- · Hot Docking

Block Diagram



Truth Table⁽¹⁾

Function	BE	A0-9
Disconnect	Н	Hi-Z
Connect	L	B ₀₋₉

Note:

 H = High Voltage Level, L = Low Voltage Level Hi-Z = High Impedance

Description

The PI3C3861 is a 10-bit, 2.5volt or 3.3 volt, 2-port bus switch designed with a low On-Resistance (5-ohm) allowing inputs to be connected directly to outputs. The bus switch creates no additional propagational delay or additional ground bounce noise. The switches are turned ON by the Bus Enable $(\overline{\rm BE})$ input signal. This device is very userful in switching signals that have high bandwidth (>400 MHz).

Pin Configuration

NC 🖂 1 ^O	24 VCC
A ₀ 2	23 <u>B</u> E
A ₁ 3	22 B ₀
A ₂ 4	21 B ₁
A ₃ 5	20 B ₂
A ₄ 6	19 B ₃
A ₅ 7	18 B ₄
A ₆ 8	17 B ₅
A7 9	16 B ₆
A ₈ 10	15 B ₇
A ₉ 11	14 B ₈
GND 12	13 B ₉

Pin Description

Pin Name	Description	
$\overline{\mathrm{BE}}$	Bus Enable Input (Active LOW)	
A ₀₋₉	Bus A	
B ₀₋₉	Bus B	
GND	Ground	
V _{CC}	Power	



Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature65°C to +150°C
Ambient Temperature with Power Applied—40°C to +85°C
Supply Voltage to Ground Potential (Inputs & Vcc Only) –0.5V to +4.6V
Supply Voltage to Ground Potential (Outputs & D/O Only)–0.5V to +4.6V
DC Input Voltage0.5V to +5.5V
DC Output Current
Power Dissipation
I and the second se

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over Operating Range, $T_A = -40^{\circ}\text{C}$ to +85°C, $V_{CC} = 3.3\text{V} \pm 10\%$)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ ⁽²⁾	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			V
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW Level	-0.5		0.8	V
I_{IH}	Input HIGH Current	$V_{CC} = Max., V_{IN} = V_{CC}$			±1	
${ m I}_{ m IL}$	Input LOW Current	$V_{CC} = Max., V_{IN} = GND$			±1	μΑ
I_{OZH}	High Impedance Output Current	$0 \le A, B \le V_{CC}$			±1	
V_{IK}	Clamp Diode Voltage	$V_{CC} = Min., I_{IN} = -18mA$		-0.73	-1.2	V
		$V_{CC} = Min., V_{IN} = 0V, I_{ON} = 48mA$		5	7	Ω
R_{ON}	Switch On-Resistance ⁽³⁾	$V_{CC} = Min., V_{IN} = 2.4V,$ $I_{ON} = 15mA$		8	15	52

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Typical values are at $V_{CC} = 3.3V$, $T_A = 25$ °C ambient and maximum loading.
- 3. Measured by the voltage drop between A and B pin at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the two (A,B) pins.

Capacitance ($T_A = 25^{\circ}C$, f = 1 MHz)

Parameters ⁽¹⁾	Description	Test Conditions	Тур.	Units
C_{IN}	Input Capacitance		3.5	
C _{OFF}	A/B Capacitance, Switch Off	$V_{IN} = 0V$	5.0	pF
C _{ON}	A/B Capacitance, Switch On		10.0	

Notes:

1. This parameter is determined by device characterization but is not production tested.

06-0245 2 PS8198E 05/09/06



Power Supply Characteristics

l	Parameters ⁽⁴⁾	Description	Test Conditions ⁽¹⁾		Min.	Typ. ⁽²⁾	Max.	Units
	I_{CC}	Quiescent Power Supply Current	$V_{CC} = Max.$	$V_{IN} = GND \text{ or } V_{CC}$		260	500	
	ΔI_{CC}	Supply Current per Input @ TTL HIGH	V _{CC} = Max.	$V_{IN} = 3.0V^{(3)}$			750	μΑ

Notes:

- 1. For Max. or Min.conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- 2. Typical values are at $V_{CC} = 3.3V$, +25°C ambient.
- 3. Per TTL driven input (control input only); A and B pins do not contribute to ICC.
- 4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

Switching Characteristics over 3.3V Operating Range

Payamatays Description		Test Conditions		Com.		
Parameters	Description	Test Conditions	Min.	Max.	Units	
t _{PLH} t _{PHL}	Propagation Delay ^(1,2) Ax to Bx, Bx to Ax			0.25		
t _{PZH} t _{PZL}	Bus Enable Time BE to Ax or Bx	$C_{L} = 50 \text{pF}$ $R_{L} = 500 \text{-ohm}$	1.5	6.5	ns	
$t_{ m PHZ} \ t_{ m PLZ}$	Bus Disable Time BE to Ax or Bx		1.5	5.5		

Notes:

- 1. This parameter is guaranteed but not tested on Propagation Delays.
- 2. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

Switching Characteristics over 2.5V Operating Range

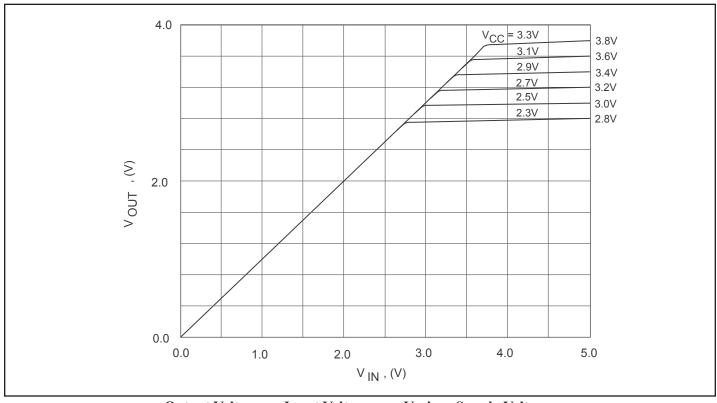
Parameters Description		Test Conditions		Com.			
Farameters	Description	Test Conditions	Min.	Max.	Units		
t _{PLH} t _{PHL}	Propagation Delay ^(1,2) Ax to Bx, Bx to Ax			0.25			
t _{PZH} t _{PZL}	Bus Enable Time BE to Ax or Bx	$C_{L} = 50 \text{pF}$ $R_{L} = 500 \text{-ohm}$	1.5	9.8	ns		
t _{PHZ} t _{PLZ}	Bus Disable Time BE to Ax or Bx		1.5	8.3			

Notes:

- 1. This parameter is guaranteed but not tested on Propagation Delays.
- 2. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

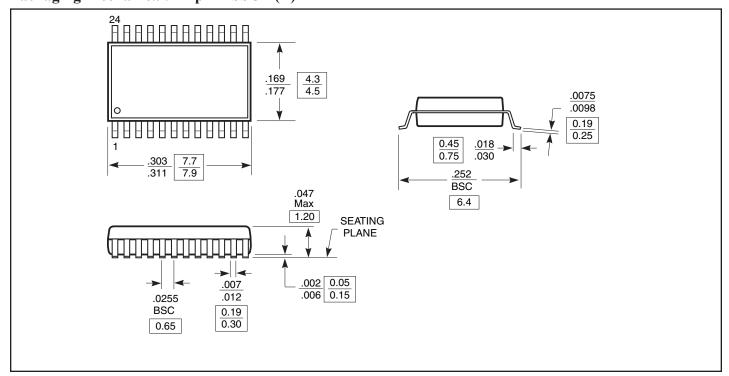
06-0245 3 PS8198E 05/09/06





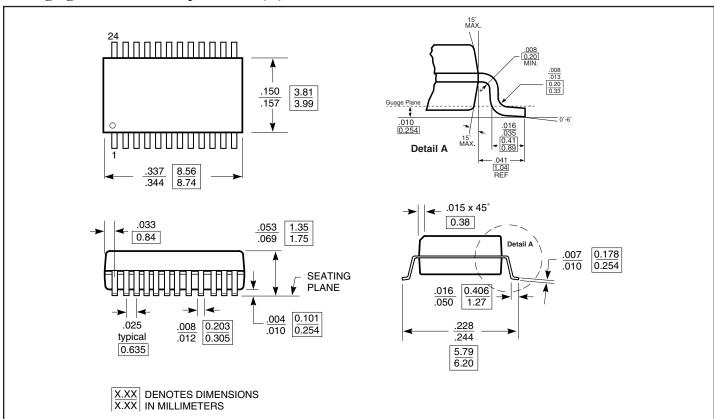
Output Voltage vs. Input Voltage over Various Supply Voltages

Packaging Mechanical: 24-pin TSSOP (L)





Packaging Mechanical: 24-pin QSOP (Q)



Ordering Information

Ordering Code	Package Code	Package Description
PI3C3861L	L	24-pin TSSOP
PI3C3861LE	L	Pb-free & Green, 24-pin TSSOP
PI3C3861Q	Q	24-pin QSOP
PI3C3861QE	Q	Pb-free & Green, 24-pin QSOP

Notes

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- E = Pb-free & Green
- Adding an X suffix = Tape/Reel

Pericom Semiconductor Corporation • 1-800-435-2336 • www.pericom.com